Digital Design Diploma

# **Assignment 2 [Extra]**

# **Combinational Circuit Design**

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| **Group** | G2 |

**Submitted to: Eng. Kareem Waseem**

1. **Gray One-Hot Encoder:**

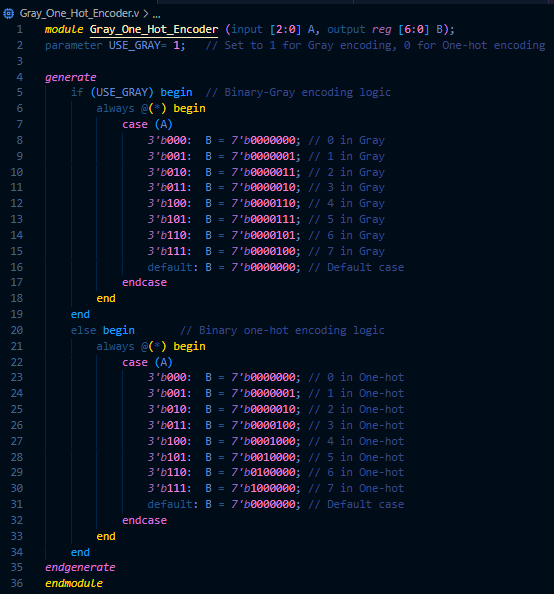
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Figure 1: Q1 Code

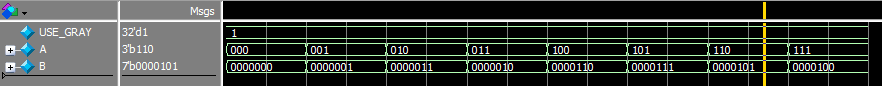
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Figure 2: Q1 Wave (Gray)

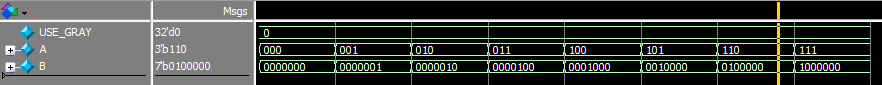
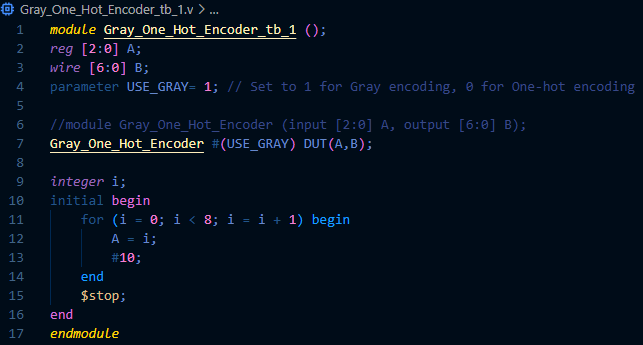
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Figure 3: Q1 Wave (One-Hot)

Figure 4: Q1 Testbench (Gray)

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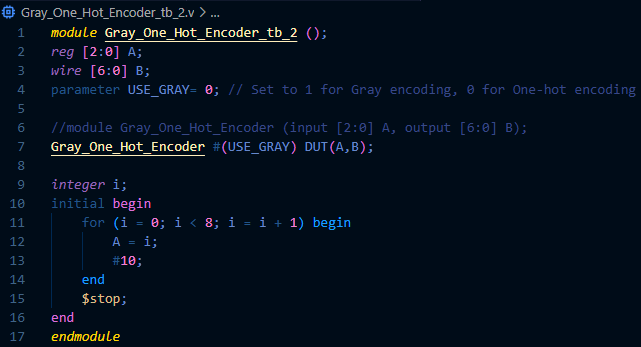
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Figure 5: Q1 Testbench (One-Hot)

1. **DEMUX:**

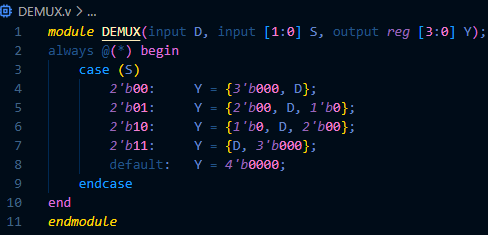
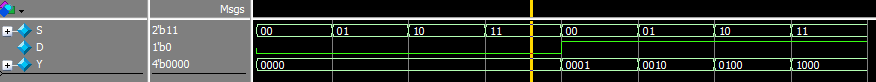
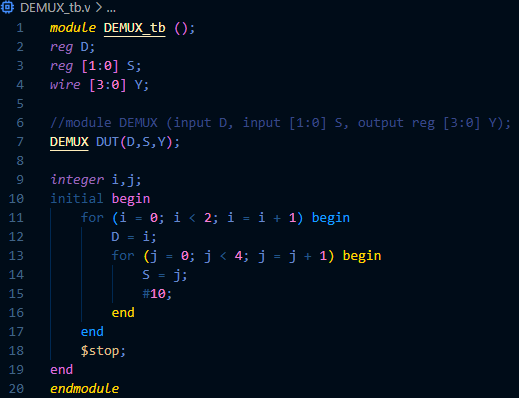


Figure 8: Q2 Wave

Figure 7: Q2 Testbench

Figure 6: Q2 Code